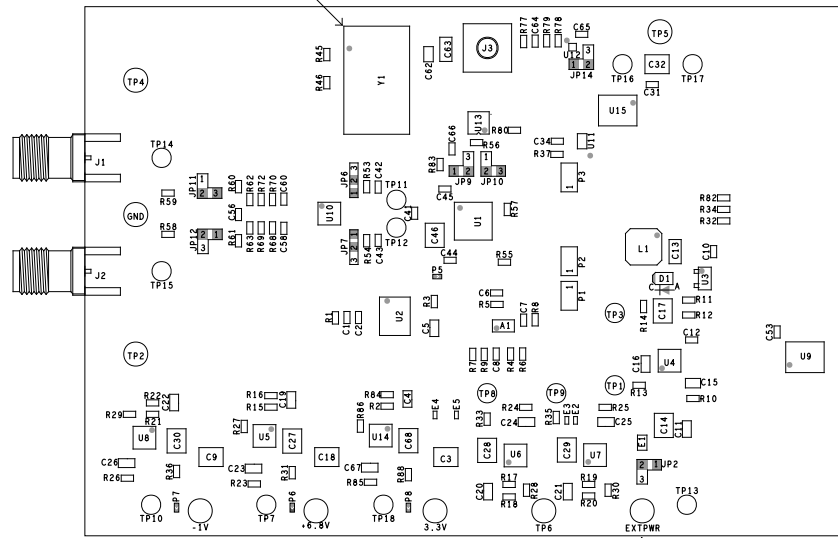


ASSEMBLY NOTES:

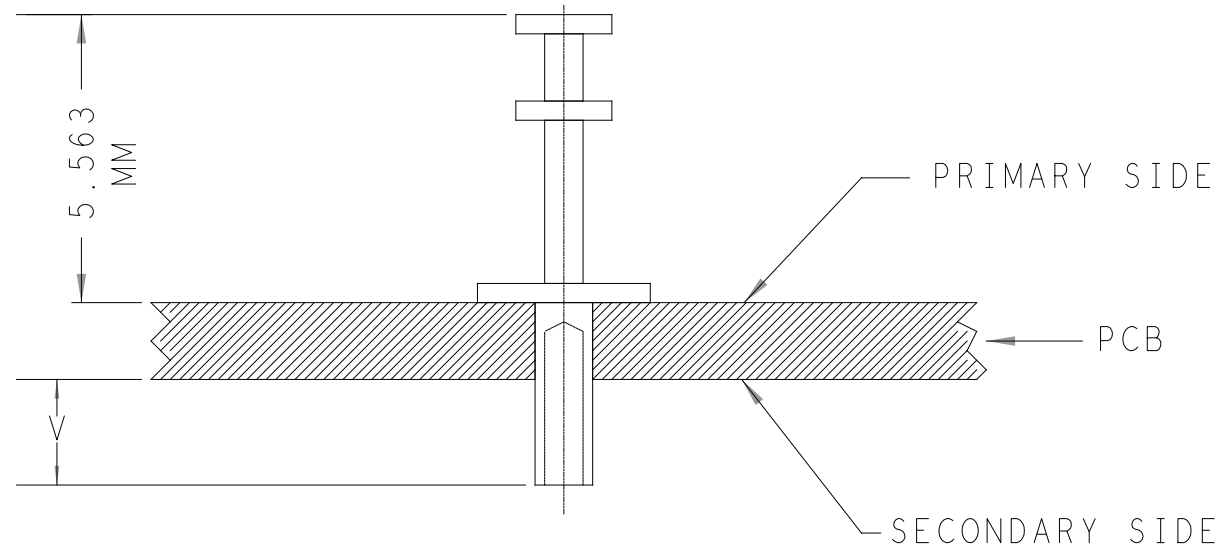
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
- SHORT JUMPER PINS 1 & 2 BY ADDING SOLDER BLOB WITH THE FOLLOWING REF DES: JP2, JP9, JP12, JP14
JP6, JP7
- SHORT JUMPER PINS 2 & 3 BY ADDING SOLDER BLOB WITH THE FOLLOWING REF DES: JP10, JP11
- P5, P6, P7, P8 SHOULD BE SHORTED BY DEFAULT BY ADDING SOLDER BLOB.
- INSTALL JUMPER SHUNT (M50-2020005) ON PIN 1 & 2 FOR THE FOLLOWING:
P1 , P3
INSTALL JUMPER SHUNT (M50-2020005) ON PIN 2 & 3 FOR THE FOLLOWING:
P2
- BOARDS TO BE SHIPPED SINGULATED AFTER ASSEMBLY PROCESS.
- SMOOTHEN EDGES AND FREE FROM BURRS AFTER DEPANELIZATION PROCESS.

SEE DETAIL B



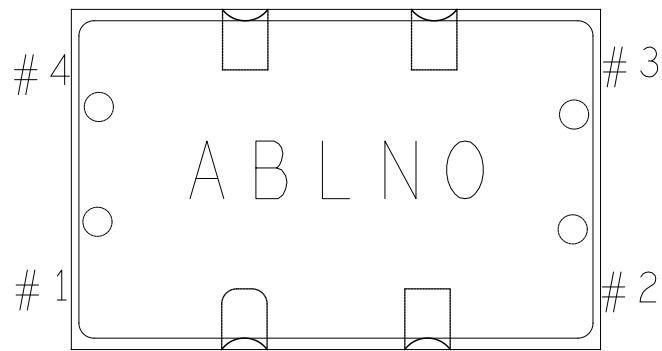
SEE DETAIL A (AT 5 PLACES)

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	07OCT21	H. DE CHAVEZ
B	CHANGES AS PER ECR-108492	05AUG22	R. EUSTACIO
B1	CHANGES AS PER ECR-111981	05DEC22	R. EUSTACIO



DETAIL A
NO TO SCALE

Note: Actual device pin #1 has a chamfered corner terminal as shown.



ABLNO-ASSY_DETAIL

DETAIL B

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050	APPROVAL		DATE		<div><div><div><div></div><div>ANALOG DEVICES</div><div>AHEAD OF WHAT'S POSSIBLE™</div></div><div>GLOBAL OPERATIONS & TECHNOLOGY 804 WOBURN STREET WILMINGTON, MA 01887</div></div></div>					
	TEMPLATE ENGINEER									
	HARDWARE SERVICES M. VALE		07OCT21							
MATERIAL	HARDWARE SYSTEMS		TITLE ASSEMBLY LTC2387 CUSTOMER EVALUATION Z							
	TEST ENGINEER									
	COMPONENT ENGINEER A. GIRON								05AUG22	
	TEST PROCESS									
FINISH	HARDWARE RELEASE K. JABATAN		07OCT21		SIZE C	FSCM NO 24355	DRAWING NUMBER 01-068279	REV B1		
	PCB DESIGNER E. DELOS REYES		07OCT21							
	PTD ENGINEER H. DE CHAVEZ		07OCT21							
	CHECKER									
DO NOT SCALE DWG					SCALE	1 / 1		SHEET	1 OF 2	

